Message Text

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TO AMEMBASSY PARIS PRIORITY

CONFIDENTIAL STATE 134949

USOECD, EXCON

E.O. 11652: XGDS-1

TAGS: ESTC, COCOM, CZ, NL

SUBJECT: NETHERLANDS CORE MEMORY STACKS TO CZECHOSLOVAKIA --IL 1588

REF: A) PARIS 16770, B) OECD PARIS 11985, C) COCOM DOC (76) 2046

- 1. IN ABSENCE OF SPECIFIC INFORMATION ON MEASURING INSTRUMENTS IN WHICH THESE CORES ARE TO BE USED, US TECHNICIANS HAVE DIFFICULTY IN DETERMINING APPROPRIATENESS OF THE CORE MEMORY STACKS FOR THE END USE. THEY APPEAR TO BE TOO FAST. THE TECHNICIANS STATE THAT:
- (1) NORMALLY THE ACCESS TIME IS TWICE THE SWITCHING TIME. FOR THIS CORE STACK THE ACCESS TIME IS ONLY 1.17 TIMES THE SWITCHING TIME. CONFIDENTIAL

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- (2) NORMALLY ACCESS TIME IS 0.50 TO 0.60 TIMES THE CYCLE TIME. FOR THIS CORE STACK THE ACCESS TIME IS ONLY 0.23 TIMES THE CYCLE TIME.
- (3) IT IS NOT CLEAR THAT A COMPUTER WILL BE USED WITH THE MEASURING INSTRUMENTATION. IF A COMPUTER IS TO BE USED,

THE COMPUTER WOULD PERFORM THE DATA STORAGE FUNCTION.

THE TECHNICIANS ALSO POINT OUT THAT THE SWITCHING TIME IS NOT PRESENTED IN THE TECHNICAL LITERATURE FROM THE MANUFACTURER, BUT IS ONLY STATED IN THE COCOM DOCUMENT.

2. USDEL THEREFORE SHOULD CONTINUE RESERVE AND REQUEST CLARIFICATION AS TO THE RELATIVE SWITCHING TIME, ACCESS TIME AND MEMORY CYCLE TIME, AND ALSO ASK WHETHER A COMPUTER WILL BE USED WITH THE MEASURING INSTRUMENTATION. VANCE

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Message Attributes

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